

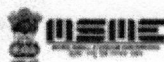


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## 2.6.2 Attainment of POs and COs are evaluated



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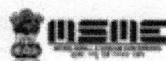


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## 2.6.2 CO Attainment




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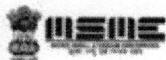


Department of Electronics and Communication Engineering	
Course Outcome Attainment	
Course Name: Electronic Devices and Circuits (C201) R16 Regulations	
At the end of the course student will be able to	
C201.1	Apply the basic concepts of semiconductor physics
C201.2	Understand the formation of p-n junction and how it can be used as a pn junction as Diode in different modes of operation
C201.3	Know the construction, working principle of rectifiers with and without filters with relevant expressions and necessary comparisons
C201.4	Understand the construction, principle of operation of transistors, BJT and FET with their V-I characteristics in different configurations
C201.5	Know the need of transistor biasing, various biasing techniques for BJT and FET and stabilization concepts with necessary expressions
C201.6	Perform the analysis of small signal low frequency transistor amplifier circuits using BJT and FET in different configurations

  
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## Program Outcomes

At the end of the Programme the students will have knowledge in:

<b>PO1</b>	<b>Engineering Knowledge:</b> Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems
<b>PO2</b>	<b>Problem analysis:</b> Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
<b>PO3</b>	<b>Design / development of solutions:</b> Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
<b>PO4</b>	<b>Conduct investigations of complex problems:</b> Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
<b>PO5</b>	<b>Modern tool usage:</b> Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
<b>PO6</b>	<b>The engineer and society:</b> Apply reasoning informed by the contextual knowledge to assess health, safety, legal and cultural issues and the consequent responsibilities relevant to the engineering practice.
<b>PO7</b>	<b>Environment and sustainability:</b> Understand the impact of the professional engineering solutions In societal and environmental contexts, and demonstrate the knowledge of and need for sustainable development.
<b>PO8</b>	<b>Ethics:</b> Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
<b>PO9</b>	<b>Individual and team work:</b> Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.




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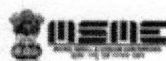


<b>PO10</b>	<b>Communication:</b> Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
<b>PO11</b>	<b>Project management and finance:</b> Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects' and in multidisciplinary environments.
<b>PO12</b>	<b>Life-long learning:</b> Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

  
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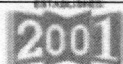
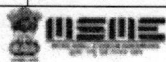
**Direct Internal Assessment - MID**

Course Name: Electronic Devices and Circuits (C201) R16 Regulations

CO-Attainment could be Calculated by

1. Internal Direct assessment	MID Marks
2. Internal Direct assessment	QUIZ+ Assignment Marks
3. Direct Assessment	Final semester end exams
4. Indirect assessment	through survey

S.No	MID Exam	MID 1 (Max. Marks 15)			MID 2 (Max. Marks 15)		
	Question Number	1	2	3	1	2	3
	CO Number	CO1	CO2	C03	C04	C05	C06
	Max.Marks	5	5	5	5	5	5
	Roll no						
1	18F91A0401	5	5	5	4	5	5
2	18F91A0402	5	5	5	5	4	4
3	18F91A0403	5	0	3	3	5	5
4	18F91A0404	5	5	5	5	5	5
5	18F91A0405						
6	18F91A0406	5	5	5	5	2	5
7	18F91A0407	5	2	2	4	2	3
8	18F91A0408	5	4	5	3	4	3
9	18F91A0409	3	4	3	4	5	5
10	18F91A0410	5	5	2	5	5	5
11	18F91A0411	5	2	5	5	4	5
12	18F91A0412	5	5	4	5	5	5
13	18F91A0413	5	3	0	4	5	5
14	18F91A0414	5	5	5	5	3	5
15	18F91A0415	5	5	2	4	2	5
16	18F91A0416	1	0	1	4	0	5
17	18F91A0417	2	3	1	5	4	4
18	18F91A0418	3	3	2	4	4	4
19	18F91A0419	4	1	5	5	0	4
20	18F91A0420	5	5	5	5	4	5
21	18F91A0421	5	3	5	5	5	5
22	18F91A0422	5	5	3	5	4	5
23	18F91A0423	3	3	5	5	2	5
24	18F91A0424	5	5	4	5	5	5
25	18F91A0425	5	5	5	4	5	5
26	18F91A0426	5	4	1	5	5	4
27	18F91A0427	5	3	3	4	1	4
28	18F91A0428	5	5	5	2	3	3
29	18F91A0429	2	5	5	5	3	4



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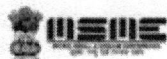


S.No	MID Exam	MID 1 (Max. Marks 15)			MID 2 (Max. Marks 15)		
	Question Number	1	2	3	1	2	3
	CO Number	CO1	CO2	CO3	CO4	CO5	CO6
	Max.Marks	5	5	5	5	5	5
	Roll no						
30	18F91A0430	2	3	3	3	3	4
31	18F91A0431	5	3	5	3	4	3
32	18F91A0432	5	3	3	4	3	4
33	18F91A0433	5	5	3	5	3	4
34	18F91A0434	4	2	3	3	3	3
35	18F91A0435	4	4	2	3	3	3
36	18F91A0436	3	3	5	5	5	4
37	18F91A0437	5	3	2	3	3	4
38	18F91A0438	5	4	1	5	3	4
39	18F91A0439	5	5	0	4	4	5
40	18F91A0440	5	4	2	4	4	4
41	18F91A0441	5	5	5	5	4	4
42	18F91A0442	5	5	4	5	5	5
43	18F91A0443	5	3	1	5	4	2
44	18F91A0444	3	2	2	4	4	2
45	18F91A0445	2	2	0	5	5	3
46	18F91A0446	5	4	2	5	5	5
47	18F91A0447	3	3	4	5	5	5
48	18F91A0448	5	4	5	4	4	3
49	18F91A0449	5	4	5	5	4	3
50	18F91A0450	4	2	3	4	4	2
51	18F91A0451	5	5	5	5	4	4
52	18F91A0452	5	5	5	3	3	3
53	18F91A0453	4	5	5	4	0	4
54	18F91A0454	5	2	3	4	0	4
55	18F91A0455	5	5	5	5	4	5
56	18F91A0456	5	0	2	4	4	4
57	18F91A0457	5	4	5	3	5	4
58	18F91A0458	5	5	5	5	4	5
59	18F91A0459	5	5	2	4	4	4
60	18F91A0460	5	3	2	5	2	4
CO Number		CO1	CO2	CO3	CO4	CO5	CO6
Attempts		59	59	59	59	59	59
Score Above 50%		54	48	39	58	49	56
Attainments %		91.52	81.35	66.1	98.3	83.05	94.91
Attainment on 3 point scale		3	3	2	3	3	3

  
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CO Number	CO1	C02	CO3	C04	CO5	C06
Attempts	59	59	59	59	59	59
Score Above 50%	54	48	39	58	49	56
Attainments %	91.52	81.35	66.1	98.3	83.05	94.91
<b>Attainment on 3 point scale</b>	<b>3</b>	<b>3</b>	<b>2</b>	<b>3</b>	<b>3</b>	<b>3</b>

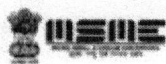
**CO-attainment Targets at course level (Direct Internal As:**

Threshold Value	
Attainments % $\geq 70$	3
Attainments % $\geq 60$	2
Attainments % $\geq 50$	1

Avg.CO Attainment	CO1	C02	CO3	C04	CO5	C06
<b>Attainment on 3 point scale</b>	<b>3</b>	<b>3</b>	<b>2</b>	<b>3</b>	<b>3</b>	<b>3</b>

Course Outcomes (COs)	Mid 1 & Mid 2
C201.1	3
C201.2	3
C201.3	2
C201.4	3
C201.5	3
C201.6	3

  
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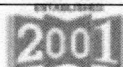
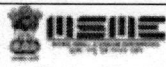
**Direct Internal Assessment (QUIZ & ASSIGNMENT)**

**Course Name: Electronic Devices and Circuits (C201) R16 Regulations**

S. No	Tool	QUIZ1	ASSIGNMENT1	QUIZ & ASSIGNMENT	QUIZ2	ASSIGNMENT2	QUIZ & ASSIGNMENT
	CO			1,2,3			4,5,6
	Max. Marks	10	5	15	10	5	15
	Roll no						
1	18F91A0401	5	5	10	1	5	6
2	18F91A0402	5	5	10	5	5	10
3	18F91A0403	6	5	11	2	5	7
4	18F91A0404	4	5	9	5	5	10
5	18F91A0405	0	0	0	0	0	0
6	18F91A0406	7	5	12	6	5	11
7	18F91A0407	5	5	10	7	5	12
8	18F91A0408	5	5	10	0	3	3
9	18F91A0409	5	5	10	6	5	11
10	18F91A0410	6	5	11	2	5	7
11	18F91A0411	7	5	12	5	5	10
12	18F91A0412	5	5	10	1	5	6
13	18F91A0413	6	5	11	6	5	11
14	18F91A0414	8	5	13	5	5	10
15	18F91A0415	1	5	6	3	5	8
16	18F91A0416	8	5	13	6	5	11
17	18F91A0417	7	5	12	4	5	9
18	18F91A0418	3	5	8	3	5	8
19	18F91A0419	5	5	10	6	5	11
20	18F91A0420	6	5	11	1	5	6
21	18F91A0421	4	5	9	5	5	10
22	18F91A0422	4	5	9	6	5	11
23	18F91A0423	6	5	11	7	5	12
24	18F91A0424	5	5	10	1	5	6
25	18F91A0425	3	5	8	1	5	6
26	18F91A0426	5	5	10	5	5	10
27	18F91A0427	5	5	10	6	5	11
28	18F91A0428	7	5	12	6	5	11
29	18F91A0429	3	5	8	5	5	10
30	18F91A0430	8	5	13	5	5	10
31	18F91A0431	8	5	13	2	5	7
32	18F91A0432	6	5	11	5	5	10
33	18F91A0433	4	5	9	3	5	8
34	18F91A0434	5	5	10	2	5	7



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*Principal*



S. No	Tool	QUIZ1	ASSIGNMENT1	QUIZ & ASSIGNMENT	QUIZ2	ASSIGNMENT2	QUIZ & ASSIGNMENT
	CO			1,2,3			4,5,6
	Max. Marks	10	5	15	10	5	15
	<b>Roll no</b>						
35	18F91A0435	5	5	10	5	5	10
36	18F91A0436	6	5	11	6	5	11
37	18F91A0437	7	5	12	6	5	11
38	18F91A0438	4	5	9	5	5	10
39	18F91A0439	4	5	9	4	5	9
40	18F91A0440	6	5	11	2	5	7
41	18F91A0441	6	5	11	5	5	10
42	18F91A0442	4	5	9	6	5	11
43	18F91A0443	7	5	12	5	5	10
44	18F91A0444	5	5	10	1	5	6
45	18F91A0445	2	5	7	6	5	11
46	18F91A0446	6	5	11	6	5	8
47	18F91A0447	9	5	14	4	5	9
48	18F91A0448	6	5	11	3	5	8
49	18F91A0449	2	5	7	5	5	10
50	18F91A0450	6	5	11	4	5	9
51	18F91A0451	3	5	8	5	5	10
52	18F91A0452	3	5	8	4	5	9
53	18F91A0453	4	5	9	5	5	10
54	18F91A0454	5	5	10	6	5	11
55	18F91A0455	5	5	10	5	5	10
56	18F91A0456	2	5	7	5	5	10
57	18F91A0457	6	5	11	6	5	11
58	18F91A0458	5	5	10	9	5	14
59	18F91A0459	6	5	11	3	5	8
60	18F91A0460	2	5	7	5	5	10

  
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Attempts	60	60	60	60	60	60
Score Above 50%	41	59	54	37	59	47
Attainments %	68.33	98.33	90.00	61.66	98.33	78.33
Attainment on 3 point scale			3			3

**CO-attainment Targets at course level (Direct Internal Assessment - QUIZ + ASSIGNMENT)**

Threshold Value	
Attainments % $\geq$ 70	3
Attainments % $\geq$ 60	2
Attainments % $\geq$ 50	1

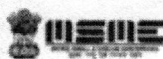
<b>AVG CO1,CO2,C03</b>	<b>3</b>
<b>AVG CO4,CO5,CO6</b>	<b>3</b>

COURSE OUTCOMES	QUIZ & ASSIGNMENT
C201.1	3.00
C201.2	3.00
C201.3	3.00
C201.4	3.00
C201.5	3.00
C201.6	3.00

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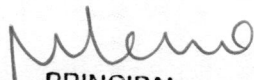


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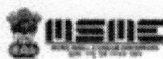
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COURSE OUT COMES	TEST	QUIZ & ASSIGNMENT	AVG
C201.1	3	3.00	3.00
C201.2	3	3.00	3.00
C201.3	2	3.00	2.50
C201.4	3	3.00	3.00
C201.5	3	3.00	3.00
C201.6	3	3.00	3.00

  
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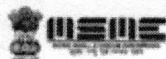


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## Final Assessment (Semester End Examinations)

Course Name: Electronic Devices and Circuits (C201) R16 Regulations

S.NO	ROLL NO.	EXTERNAL GRADE
1	18F91A0401	C
2	18F91A0402	B
3	18F91A0403	F
4	18F91A0404	B
5	18F91A0406	F
6	18F91A0407	D
7	18F91A0408	F
8	18F91A0409	F
9	18F91A0410	D
10	18F91A0411	B
11	18F91A0412	A
12	18F91A0413	D
13	18F91A0414	C
14	18F91A0415	C
15	18F91A0416	F
16	18F91A0417	D
17	18F91A0418	F
18	18F91A0419	C
19	18F91A0420	C
20	18F91A0421	B
21	18F91A0422	D
22	18F91A0423	A
23	18F91A0424	B
24	18F91A0425	A
25	18F91A0426	F
26	18F91A0427	B
27	18F91A0428	F
28	18F91A0429	F
29	18F91A0430	F
30	18F91A0431	B
31	18F91A0432	B
32	18F91A0433	B
33	18F91A0434	F
34	18F91A0435	F
35	18F91A0436	F
36	18F91A0437	F



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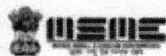
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S.NO	ROLL NO.	EXTERNAL GRADE
37	18F91A0438	F
38	18F91A0439	C
39	18F91A0440	B
40	18F91A0441	S
41	18F91A0442	B
42	18F91A0443	A
43	18F91A0444	D
44	18F91A0445	D
45	18F91A0446	D
46	18F91A0447	C
47	18F91A0448	D
48	18F91A0449	B
49	18F91A0450	F
50	18F91A0451	S
51	18F91A0452	B
52	18F91A0453	D
53	18F91A0454	F
54	18F91A0455	S
55	18F91A0456	B
56	18F91A0457	C
57	18F91A0458	C
58	18F91A0459	B
59	18F91A0460	C

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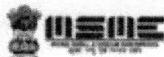
## CO'S attained targets at course level

PREDEFINED THRESHOLD VALUE		
LEVEL 1	LESS THAN 50%	1
LEVEL 2	INBETWEEN 51% AND 59%	2
LEVEL 3	60% AND MORE	3

ATTEMPTS	59
SCORES ABOVE F GRADE	42
ATTINMENT %	71.18

COURSE OUT COMES	END EXAM
C201.1	3
C201.2	3
C201.3	3
C201.4	3
C201.5	3
C201.6	3

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**CO – Attainment**

COs-attainment evaluation is the combination of Direct and In-Direct attainment

I) Direct-80%      II) Indirect-20%


I. Direct attainment process is the combination of

- 1) Internal exam marks    a) Descriptive Exam Marks    b) Quiz & Assignment marks
- 2) External Examination marks

**Total Direct CO-Attainment= 80% External Exam Marks + 20% Internal Exam Marks**

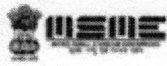
II. In-Direct attainment process is evaluated through Course Exit Survey

COURSE OUT COMES	TEST	QUIZ & ASSIGNMENT	AVG	END EXAM	ATTAINMENT	Direct CO-80%	In- Direct CO%	In-Direct CO-20%	Total CO Attainment
C201.1	3	3.00	3.00	3	3.00	2.40	3	0.6	3
C201.2	3	3.00	3.00	3	3.00	2.40	3	0.6	3
C201.3	2	3.00	2.50	3	2.90	2.32	3	0.6	2.92
C201.4	3	3.00	3.00	3	3.00	2.40	3	0.6	3
C201.5	3	3.00	3.00	3	3.00	2.40	3	0.6	3
C201.6	3	3.00	3.00	3	3.00	2.40	3	0.6	3
								<b>AVG</b>	<b>2.98</b>

  
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### CO-PO MAPPING

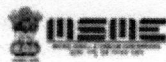
All course outcomes shall have linkage to Programme outcomes is such as a way that strongest relation has the weight 3 and the weakest relation is 1. The relation is put into a table given below.

C0	P01	P02	P03	P04	P05	P06	P07	P08	P09	P010	P011	P012
C201.1	3	3	2	2	-	-	-	-	-	-	-	2
C201.2	3	3	2	-	-	-	-	-	-	-	-	2
C201.3	3	3	2	2	-	-	-	-	-	-	-	2
C201.4	3	3	2	3	-	-	-	-	-	-	-	2
C201.5	3	3	3	-	-	-	-	-	-	-	-	2
C201.6	3	3	2	-	-	-	-	-	-	-	-	2
AVG C201	3	3	2.16	2.33	-	-	-	-	-	-	-	2

  
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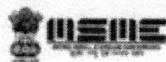
## 2.6.2 CO-PO ATTAINMENT

C0	P01	P02	P03	P04	P05	P06	P07	P08	P09	P010	P011	P012
C201DA	2.98	2.98	2.14	2.31	-	-	-	-	-	-	-	1.98
C211IDA	3	3	3	3	-	-	-	-	-	-	-	3
TOTAL POA	2.98	2.98	2.31	2.44	-	-	-	-	-	-	-	2.18

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